

What is claimed is:

1. A plasma display panel including a sustaining electrode pair of a transparent conductive material provided on an upper substrate, and a display region coexisting with a non-display region, wherein a distance between the sustaining electrode pair at the display region is different from that at the non-display region.

2. The plasma display panel as claimed in claim 1, wherein the sustaining electrode pair at the non-display region has each longitudinal rounded in such a manner that a length of the opposed side thereof becomes distant.

3. A plasma display panel including a sustaining electrode pair of a transparent conductive material provided on an upper substrate, barrier ribs formed on a lower substrate in a direction crossing the sustaining electrode pair, and a display region coexisting with a non-display region, wherein a width of the barrier rib at the display region is different from that at the non-display region.

4. The plasma display panel as claimed in claim 3, wherein the barrier rib at the non-display region has a larger width than that at the display region.

5. The plasma display panel as claimed in claim 3, wherein the barrier rib at the non-display region is set to have a large width than each end of the sustaining electrode pair overlapping with itself.

6. A plasma display panel including a sustaining

electrode pair of a transparent conductive material provided on an upper substrate, barrier ribs formed on a lower substrate in a direction crossing the sustaining electrode pair, and a display region coexisting with a non-display region, wherein the non-display region is provided with black matrices for shutting off a light.

7. The plasma display panel as claimed in claim 6, wherein the black matrices are arranged in parallel to the barrier ribs.

8. The plasma display panel as claimed in claim 7, wherein the black matrices are formed at each longitudinal end of the barrier ribs in a direction crossing the barrier ribs.

9. A plasma display panel including an upper substrate, a protective layer provided at the rear side of the upper substrate, and a display region coexisting with a non-display region, wherein the protective layer is provided only at the display region.